

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Shubhendu S. MUKHERJEE §
Serial No.: 09/839,624 § Group Art Unit: 2183
Filed: April 19, 2001 § Examiner: Unknown
For: Load Value Queue Input §
Replication In A Simultaneous §
And Redundantly Threaded §
Processor §

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Technology Center 2100

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Atty. Dkt. No. 1662-37500
Client Dkt No. P00-3162
Date: April 11, 2002

Sir:

In accordance with 37 CFR §§ 1.97, 1.98, Applicants are providing herewith copies of the supplementary items listed on the attached U.S. Patent and Trademark Office Form PTO-1449. This information is supplemental to the Information Disclosure Statement and Form PTO-1449 filed in the above-referenced case on April 19, 2001.

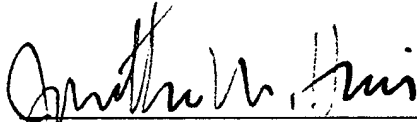
The submission of this Supplemental Information Disclosure Statement and Form PTO-1449 is not an admission that the art cited is "prior" with respect to the present invention, nor is it a representation that no better art exists. Applicants hereby reserve the right to swear behind or otherwise disprove any alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is submitted that the art cited does not constitute a bar to the patentability of Applicant's invention under 35 U.S.C. § 102 or § 103.

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No Office Action on the merits has been received in the present application, therefore no fee is due for the filing of this Supplemental Information Disclosure Statement. Nonetheless, in the event that an Office Action dated PRIOR to the mailing date of this Supplemental Information Disclosure Statement has been issued, please charge Deposit Account 03-2769, Conley, Rose & Tayon, P.C., in the amount of \$180, so that this Supplemental Information Disclosure Statement may be considered under Rule 1.97(c).

Respectfully submitted,



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